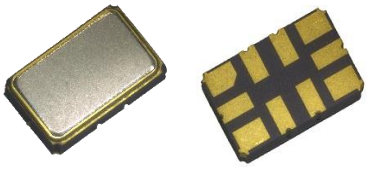
High Stability Temperature Compensated Crystal Oscillator (TCXO)
Supports ±0.1× 10^{-6} over -40 ºC to +105 ºC

TG5032CKN / TG5032SKN



TG5032CKN
TG5032SKN
（10 pins）

Key Specifications

Frequency Range:	10 MHz to 54 MHz
Supply Voltage:	3.3 V Typ.
Operation Temperature:	-40 ºC to +105 ºC
Freq. / Temp characteristic:	±0.1 × 10^{-6} Max. over temperature
Frequency Slope:	±0.05 × 10^{-6} / ºC Max. over temperature
Frequency Aging:	±3.0 × 10^{-6} Max. / 20 years (meets Stratum3)
Package:	5.0 × 3.2 × 1.45 mm (10 pins)

With on-chip filter capacitor for lower phase noise

Description - TG5032CKN and TG5032SKN is a high stability TCXO available with either CMOS or Clipped Sine output

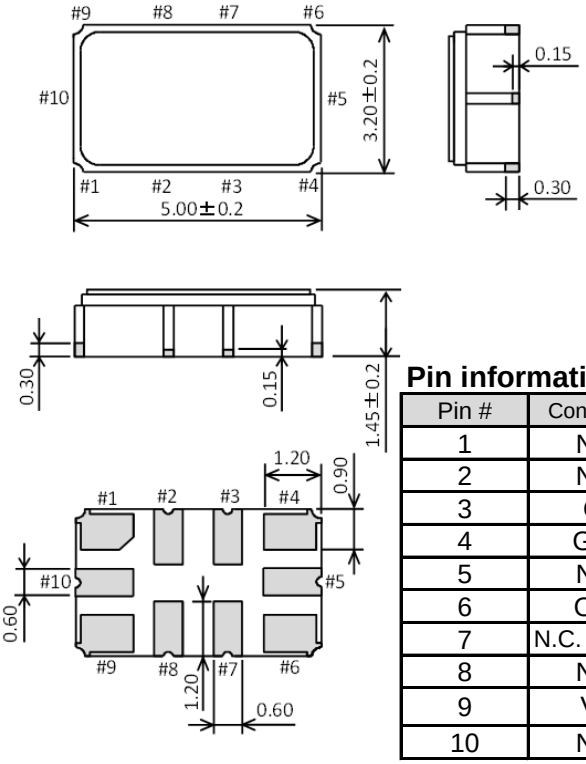
- Specified to +105 ºC for 5G Base Stations and Edge Computing needs outdoor installation, miniaturization, and fan-less operation
- Compared to other TCXOs it offers a variety of improvements such as low temperature slope and phase noise
- Complies with GR-1244-CORE Stratum3 and G.8262.1, G.8273.2 (Class A, B)
- Note: this product does not have a voltage control (Vc) function

Applications

Network equipment
- Base station
- Microwave

Sync compliance standards
- Stratum3
- SyncE
- IEEE1588

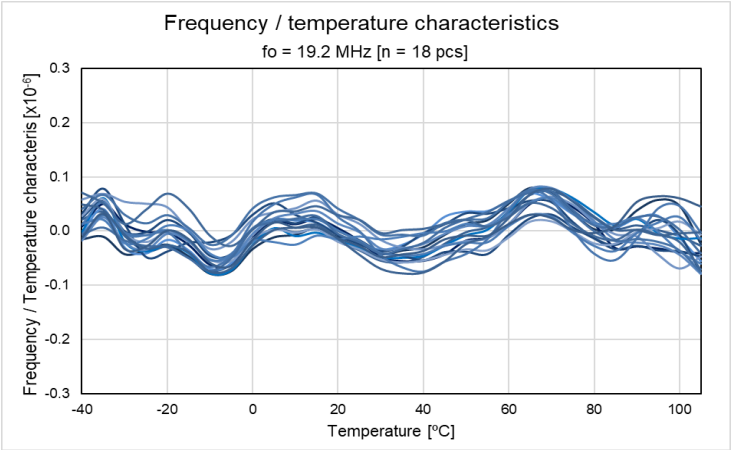
Outline dimensions

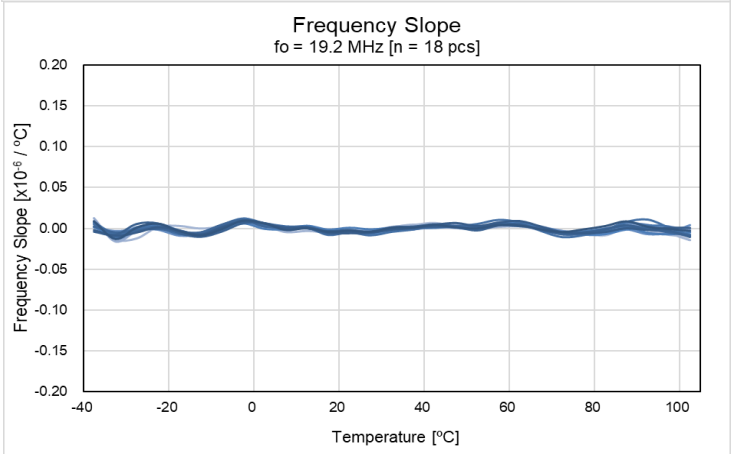


Pin information

Pin #	Connection
1	N.C.
2	N.C.
3	OE
4	GND
5	N.C.
6	OUT
7	N.C. or Filter
8	N.C.
9	V_{CC}
10	N.C.

Characteristics





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[1] Product Number / Product Name

(1) Product Name

TG5032	C	K	N	38.880000MHz	C	A	H	H	G	A
①	②	③		④	⑤	⑥	⑦	⑧	⑨	⑩

①Model ②Output (C: CMOS, S: Clipped sine) ③Package type (K: 10 pins) ④Frequency
⑤Supply voltage (C: 3.3 V Typ.) ⑥Frequency/temperature characteristics (A: ±0.1 × 10^{-6} Max.)
⑦Operating temperature (H: -40 ºC to +105 ºC, G: -40 ºC to +85 ºC)
⑧OE function (H: Active High) ⑨Filter function (G: Filter-ON, N: NO-Filter) ⑨Internal identification code (“A” is default)

(2) Product Number / Ordering Code

TG5032CKN: X1G006021xxxx14 (CMOS)
TG5032SKN: X1G006031xxxx14 (Clipped sine)

* Please contact Epson for detail of product number

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[2] Absolute Maximum Ratings

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Supply voltage	V_{CC}-GND	-0.5	-	+4.0	V	
Input voltage	V_{IN}	-0.5		V_{CC} + 0.5	V	OE Terminal
Storage temperature range	T_stg	-40	-	+105	°C	Storage as single product

[3] Operating Conditions

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Supply voltage	V_{CC}	3.135	3.3	3.465	V	V_{CC} = 3.3 V ± 5 %
	GND	0	-	0	V	
Input voltage	V_{IN}	GND	-	V_{CC}	V	OE Terminal
Operating temperature range	T_use	-40	+25	+105	°C	
CMOS load condition	L_CMOS	13.5	15	16.5	pF	
Output load (Clipped sine)	Load_R	9	10	11	kΩ	
	Load_C	9	10	11	pF	
	Cc	0.01	-	-	µF	AC coupling capacitor *

* AC coupling capacitor is not included in this TCXO, please attach an external AC coupling capacitor to the out pin.

[4] Frequency Characteristics

(V_{CC} = 3.3 V, GND = 0.0 V, T_use = +25 °C)

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Output Frequency	fo	10	-	54	MHz	
Frequency tolerance *1	f_tol	-1.0	-	+1.0	x10^{-6}	T_use = +25 °C ± 2 °C After 3 reflows *2
Frequency / temperature characteristics	fo-Tc	-0.1	-	0.1	x10^{-6}	T_use = -40 °C to +105 °C, Reference to (fmax+fmin) / 2
Frequency slope	f_slp	-0.05	-	0.05	x10^{-6} / °C	T_use = -40 °C to +105 °C, One frequency reading every 5 °C Min.
Frequency / load coefficient	fo-Load	-0.1	-	+0.1	x10^{-6}	Load_R // Load_C ± 10 % L_CMOS ± 10 %
Frequency / voltage coefficient	fo-V_{CC}	-0.1	-	+0.1	x10^{-6}	V_{CC} = 3.3 V ± 5 %
Frequency aging *3	f_age	-0.5	-	+0.5	x10^{-6}	T_use = +25 °C, first year
Frequency aging *3	f_age	-3.0	-	+3.0	x10^{-6}	T_use = +25 °C, 20 years
Holdover stability (Free-run accuracy) *4	f_hos	-4.6	-	+4.6	x10^{-6}	T_use = +25 °C, 20 years

*1 Include initial frequency tolerance and frequency deviation after reflow cycles.

*2 Measured 24 hours after reflow soldering.

*3 Aging is estimated from environmental reliability tests and the expected amount of frequency variation over time. It is not intended as a guarantee of performance over the product-life cycle.

*4 This includes initial frequency tolerance, frequency / temperature characteristics, frequency / load coefficient, frequency/voltage coefficient and frequency aging (+25 °C, 20 years) .

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[5] Electrical Characteristics

1) Electrical Characteristics

(V_{CC} = 3.135 V to 3.465 V, GND = 0.0 V, T_use = +25 °C)

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Current consumption (CMOS)	I_{CC}	-	-	7 9 10	mA	10 MHz ≤ fo ≤ 26 MHz 26 MHz < fo ≤ 40 MHz 40 MHz < fo ≤ 54 MHz
Current consumption (Clipped sine)	I_{CC}	-	-	6	mA	
Output voltage (CMOS)	V_{OH}	90 % V_{CC}	-	-	V	
Output voltage (CMOS)	V_{OL}	-	-	10 % V_{CC}	V	
Output voltage (Clipped sine)	Vpp	0.8	-	-	V	Peak to peak voltage
Rise time (CMOS)	tr	-	-	8	ns	10 % V_{CC} to 90 % V_{CC} level
Fall time (CMOS)	tf	-	-	8	ns	90 % V_{CC} to 10 % V_{CC} level
Start-up time	t_str	-	-	5	ms	Until output signal has been reached min 90 % of final amp.
Symmetry (CMOS)	SYM	45	50	55	%	50 % V_{CC} Level
Phase noise (fo = 19.2 MHz) * Filter-on	L(f)	-	-69	-	dBc/Hz	1 Hz offset
		-	-100	-		10 Hz offset
		-	-126	-		100 Hz offset
		-	-145	-		1 kHz offset
		-	-155	-		10 kHz offset
		-	-158	-		100 kHz offset
		-	-158	-		1 MHz offset

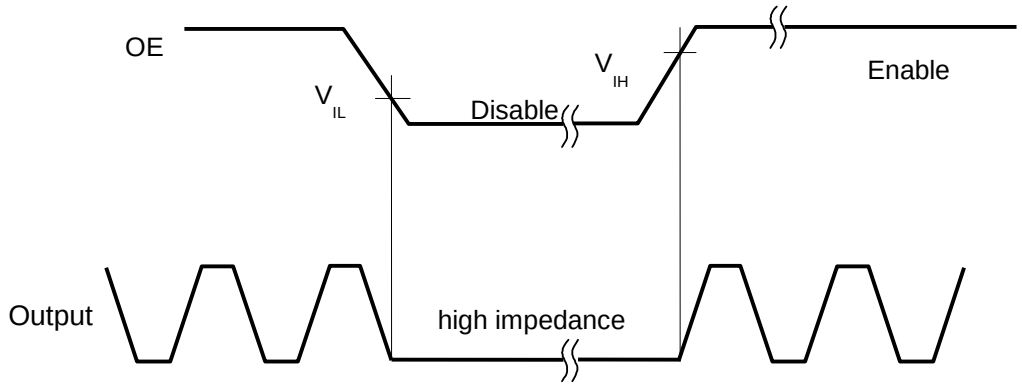
* For other frequencies, please refer to Charts (7-10), Phase noise

2) Enable/Disable Function

Parameter	Symbol	Specifications			Unit	Conditions
		Min.	Typ.	Max.		
Input voltage	V_{IH}	70 % V_{CC}	-	V_{CC}	V	OE terminal (Enable voltage)
Input voltage	V_{IL}	0	-	30 % V_{CC}	V	OE terminal (Disable voltage)
Input impedance	Zin	50	-	-	kΩ	

OE input level	Oscillation	Outputs
"H" or "Open"	Enable	Enable : specified frequency
"L"	Enable	Disable : high impedance

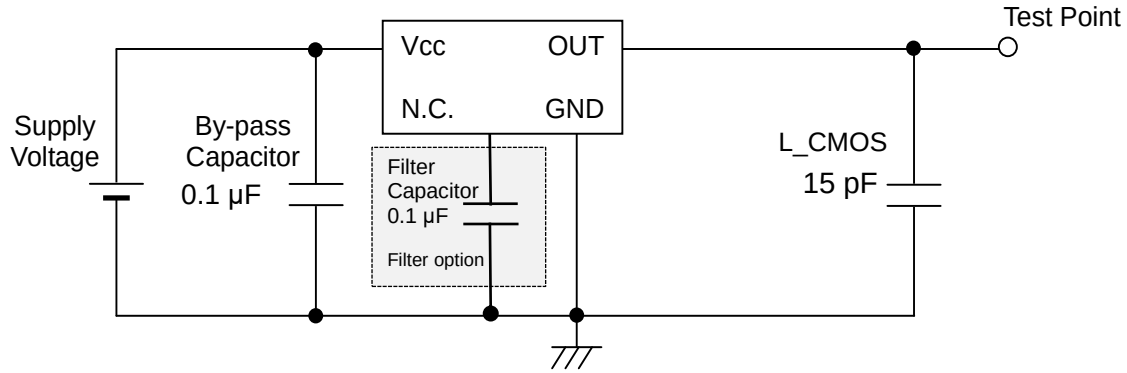
* OE input voltage must be lower than V_{CC}. Note that rise-up time of OE input voltage must not be shorter than the rise-up time of supply voltage.



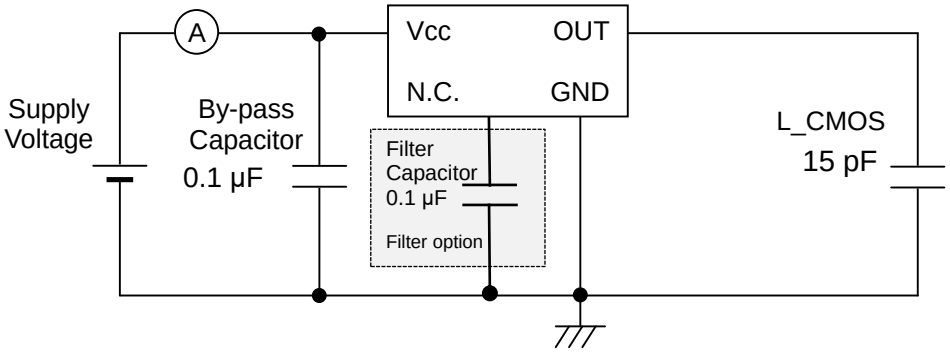
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[6] Test Circuit: CMOS-Output

1) Output Waveform



2) Current Consumption



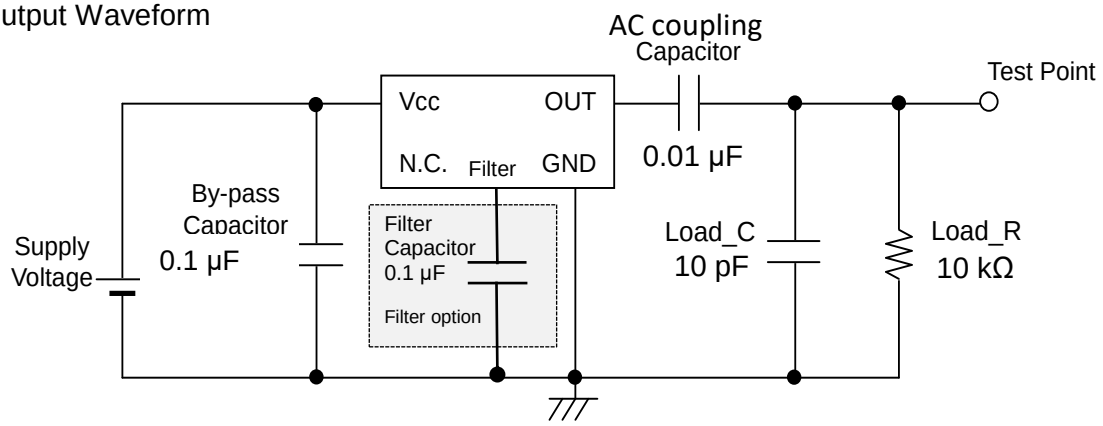
3) Conditions

1. Oscilloscope: Impedance Min. 1 MΩ
   Input capacitance Max. 10 pF
   Band width Min. 300 MHz
2. L_CMOS includes probe capacitance
3. A capacitor (By-pass: 0.1 µF) is placed between V_{CC} and GND,and closely to TCXO
4. Use the current meter whose internal impedance value is small.
5. Power supply connections should be as low impedance as possible
6. GND pin should be connected to low impedance GND

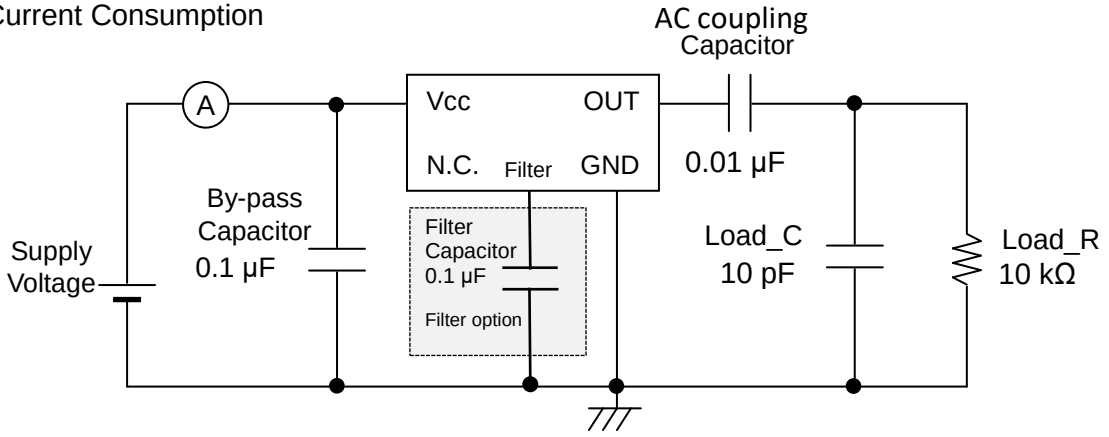
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[6] Test Circuit: Clipped Sine Wave-Output

1) Output Waveform



2) Current Consumption



3) Conditions

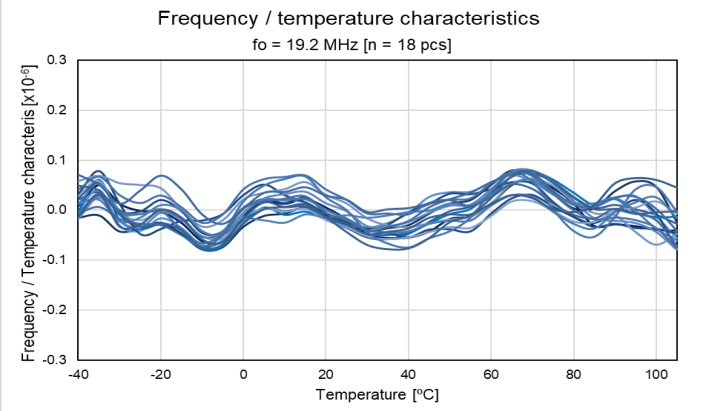
1. Oscilloscope: Impedance Min. 1MΩ
   Input capacitance Max. 10 pF
   Band width Min. 300 MHz
2. Load_C includes probe capacitance
3. A capacitor (By-pass: 0.1 µF) is placed between V_{CC} and GND,and closely to TCXO
4. Use the current meter whose internal impedance value is small.
5. Power supply connections should be as low impedance as possible
6. GND pin should be connected to low impedance GND

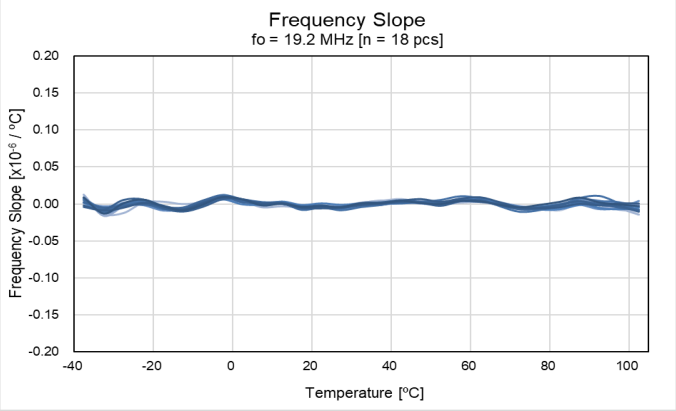
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[7] Characteristic Data

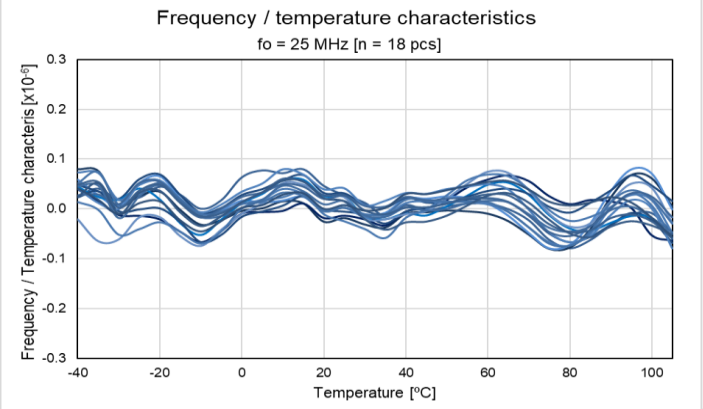
(7-1) Frequency / Temperature Characteristics & Frequency Slope

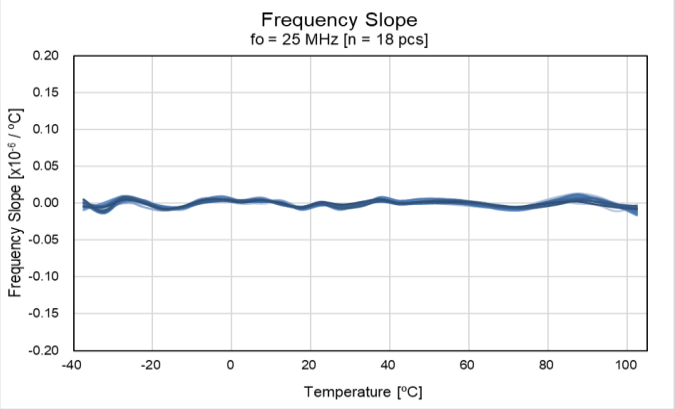
1) fo = 19.2 MHz



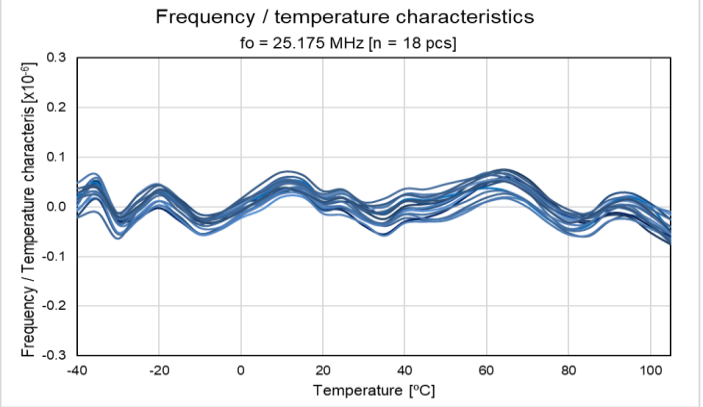


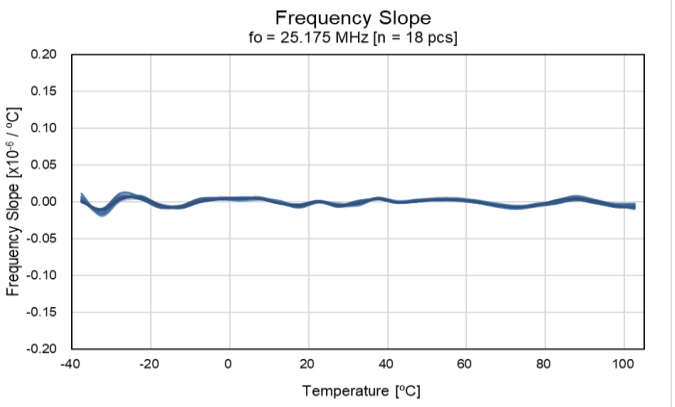
2) fo = 25 MHz



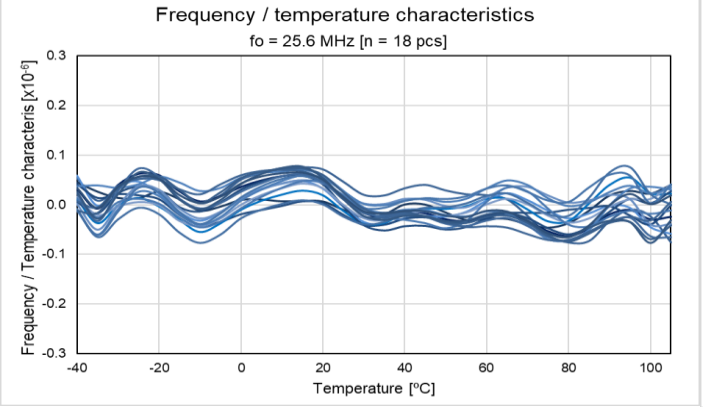


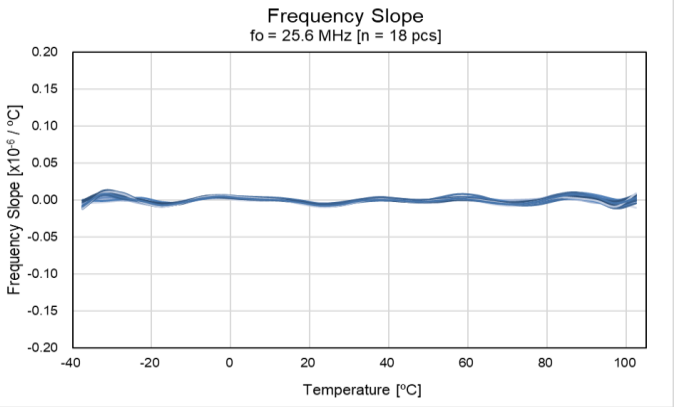
3) fo = 25.175 MHz





4) fo = 25.6 MHz

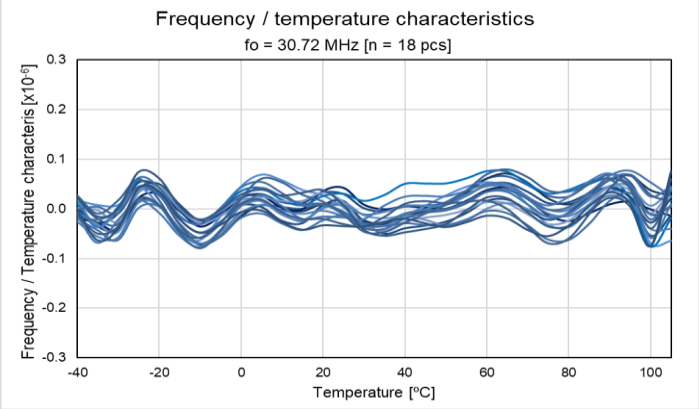


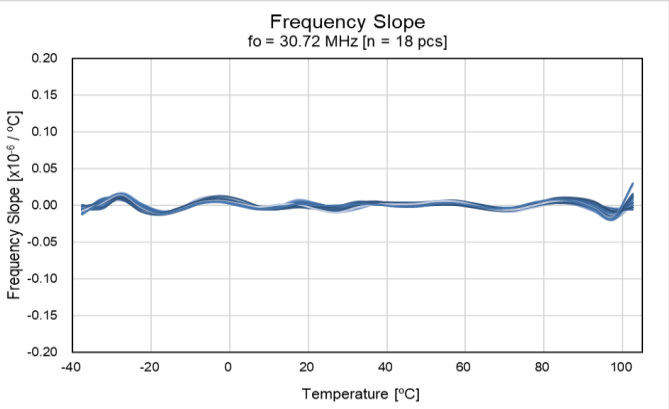


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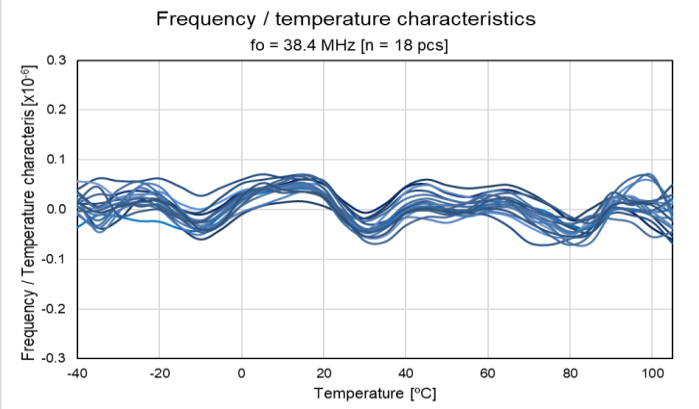
(7-1) Frequency / Temperature Characteristics & Frequency Slope

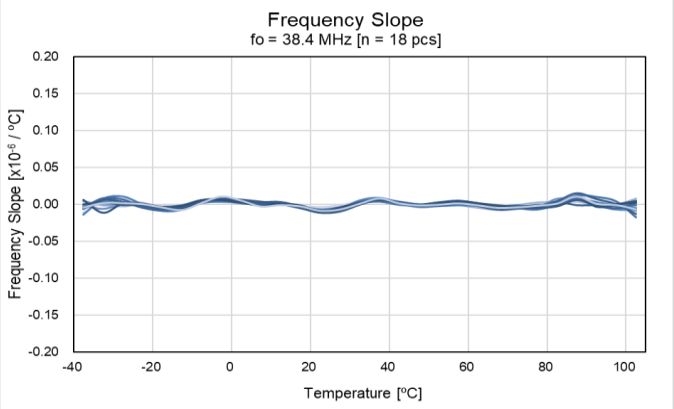
5) fo = 30.72 MHz



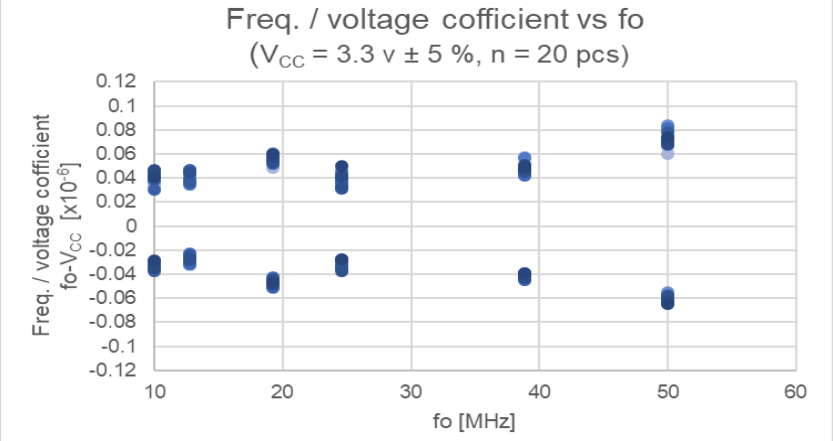


6) fo = 38.4 MHz





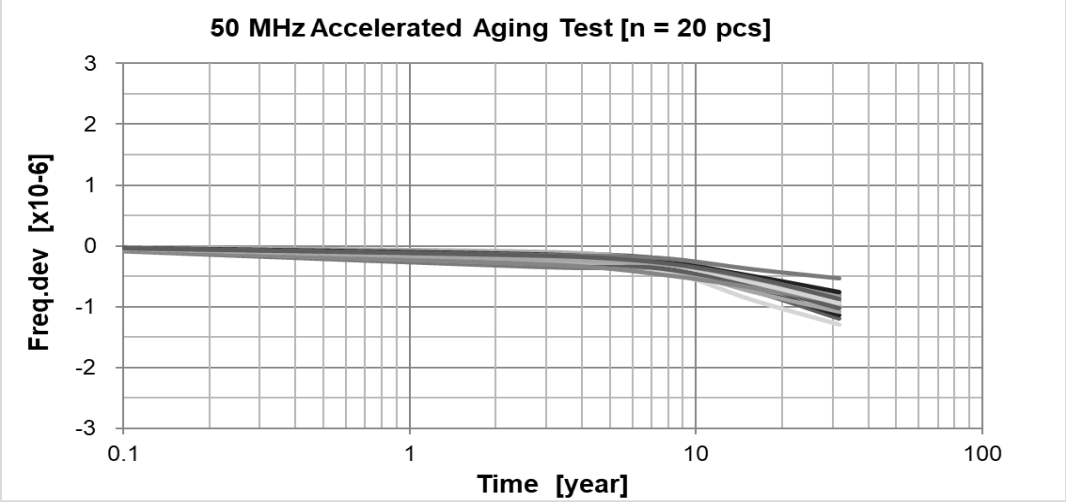
(7-2) Frequency / Voltage Coefficient



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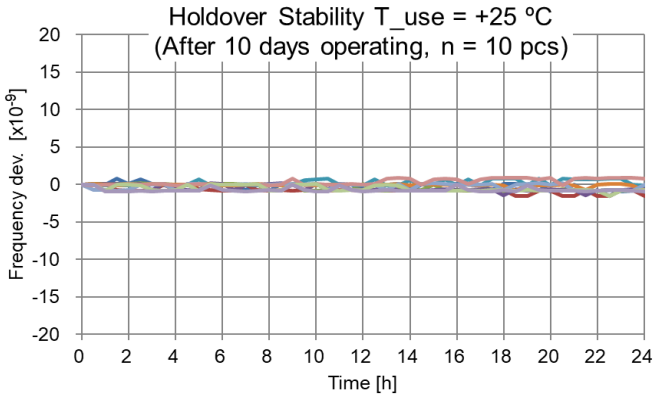
(7-3) Frequency Aging Conversion

* Aging stability is estimated from environmental reliability tests and the expected amount of frequency variation of the product.
It is not intended as a guarantee of performance over the product-life cycle.

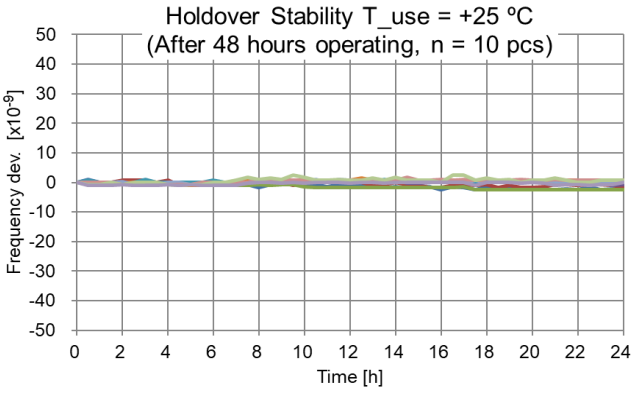


(7-4) Holdover

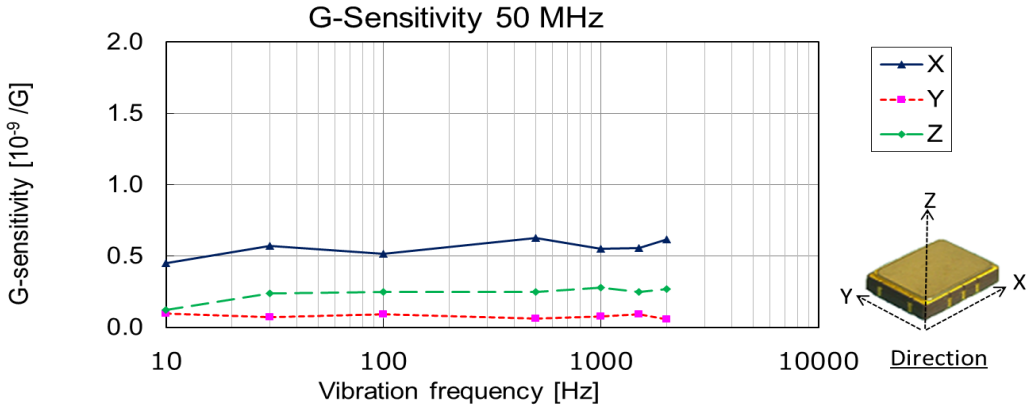
1) After 10 days



2) After 48 hours

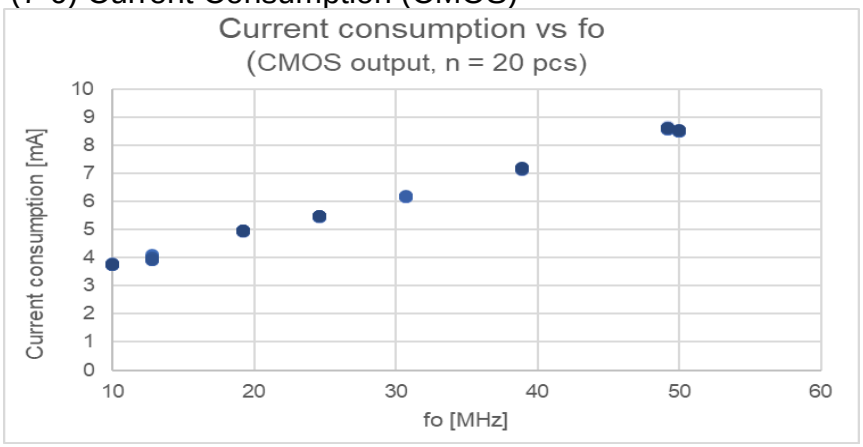


(7-5) G-Sensitivity

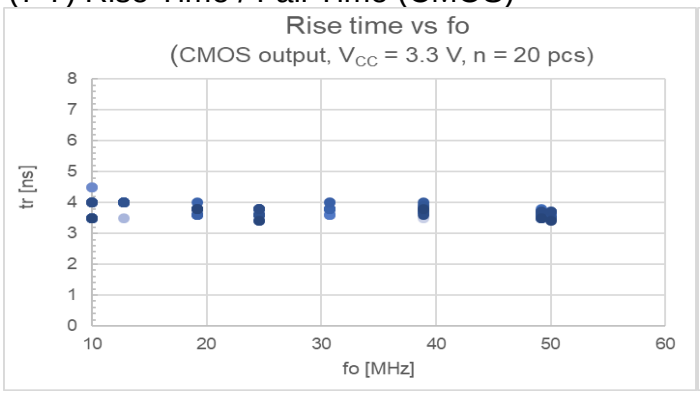


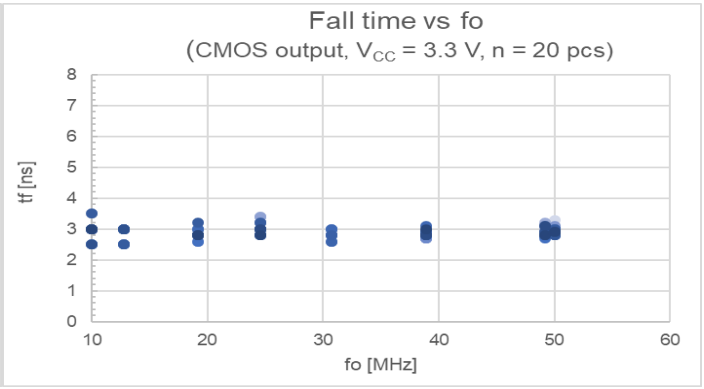
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(7-6) Current Consumption (CMOS)

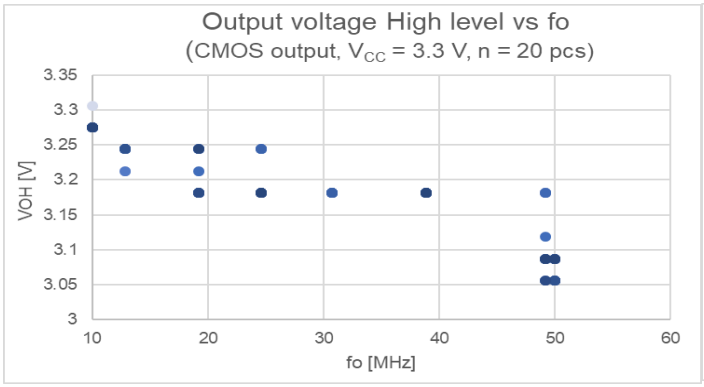


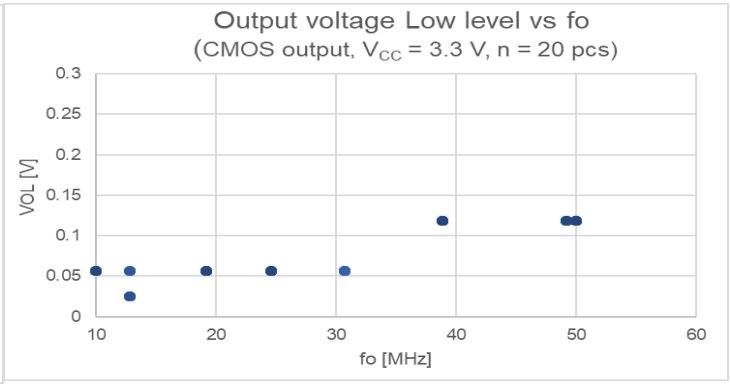
(7-7) Rise Time / Fall Time (CMOS)



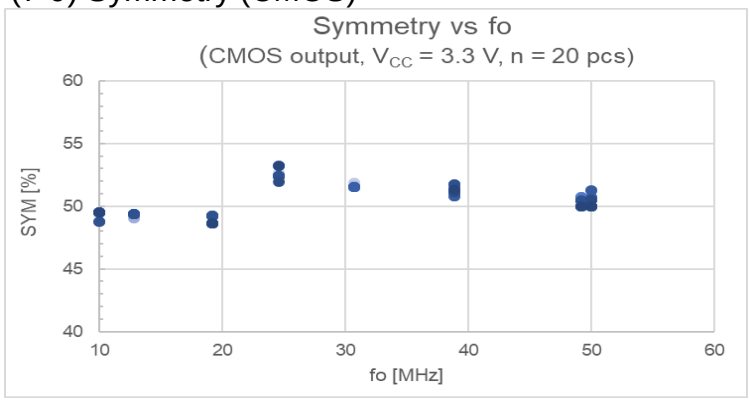


(7-8) Output Voltage [V_{OH}, V_{OL}] (CMOS)





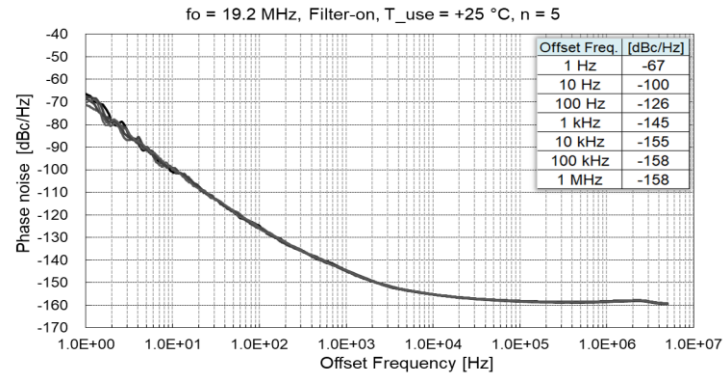
(7-9) Symmetry (CMOS)

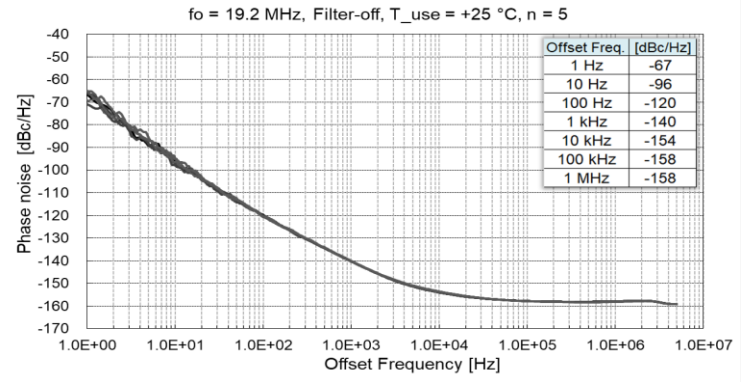


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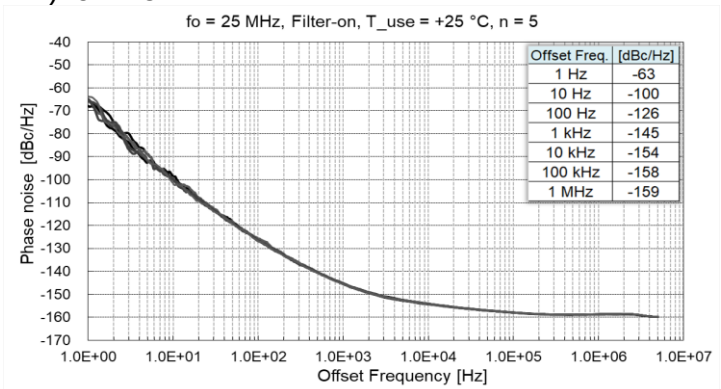
(7-10) Phase Noise * Filter-ON requires the option setting of Filter-ON and the connection of the phase noise filter capacitor 0.1 µF (recommended) to Pin7.

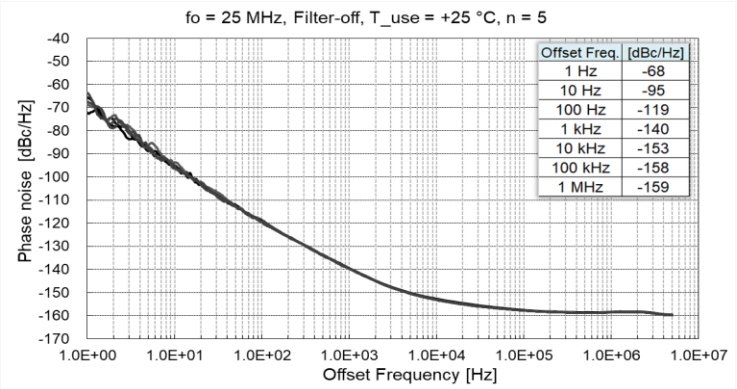
1) fo = 19.2 MHz



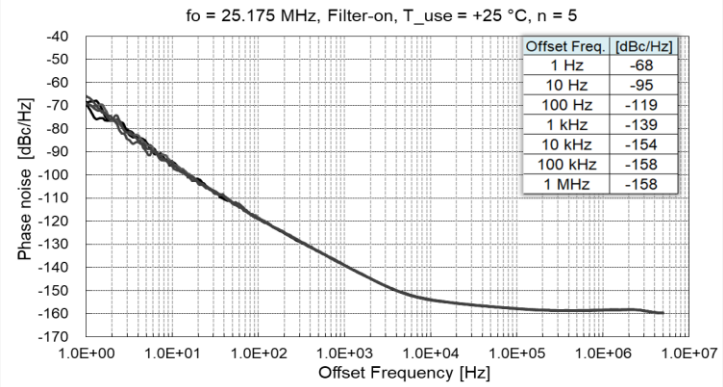


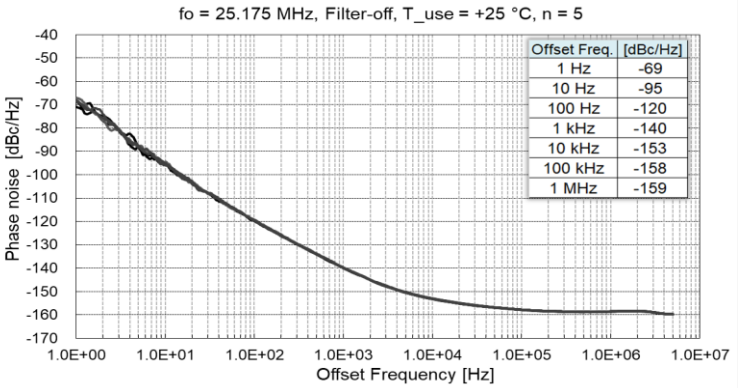
2) fo = 25 MHz



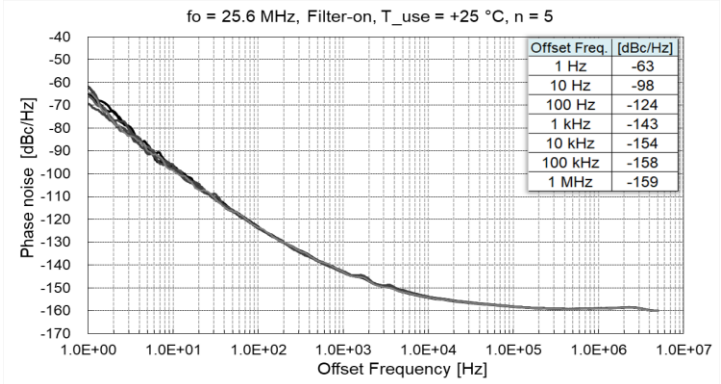


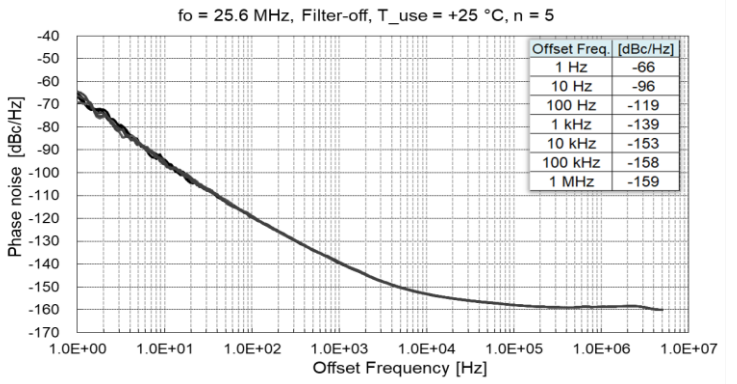
3) fo = 25.175 MHz





4) fo = 25.6 MHz

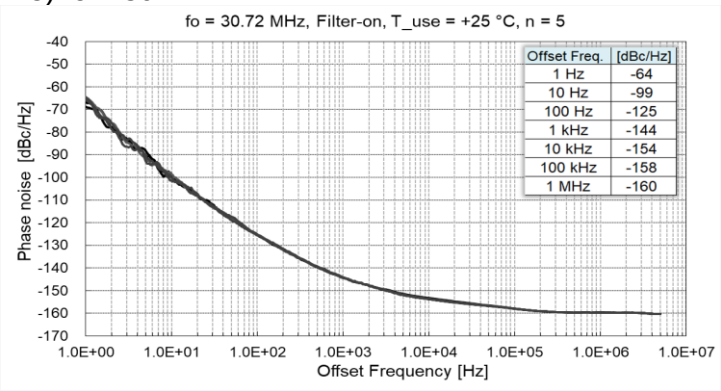


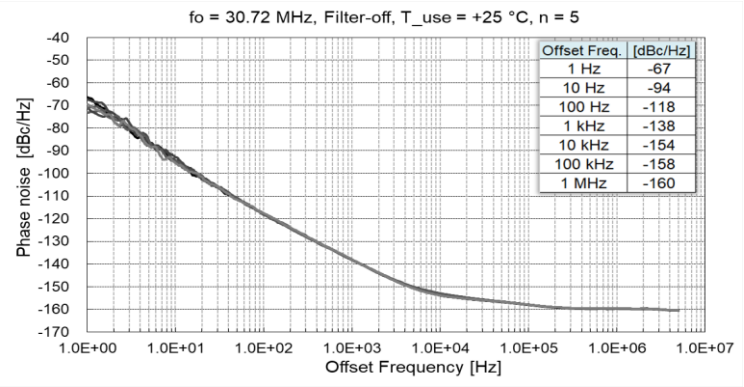


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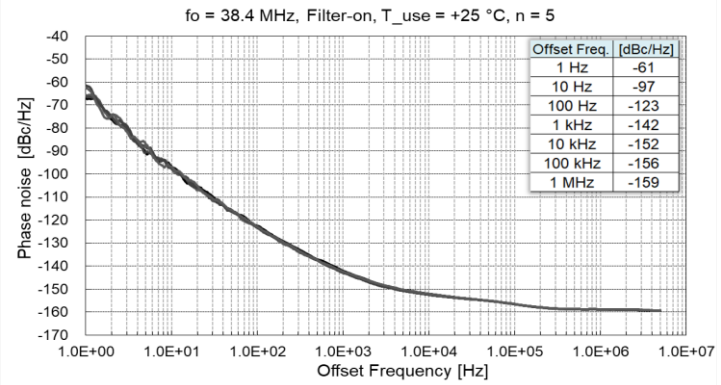
(7-10) Phase Noise * Filter-ON requires the option setting of Filter-ON and the connection of the phase noise filter capacitor 0.1 µF (recommended) to Pin7.

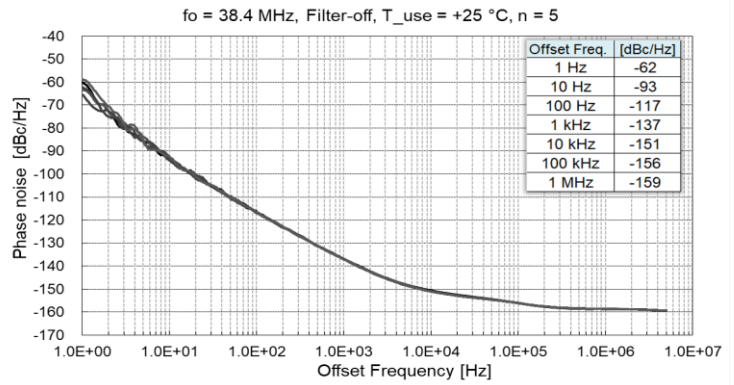
5) fo = 30.72 MHz



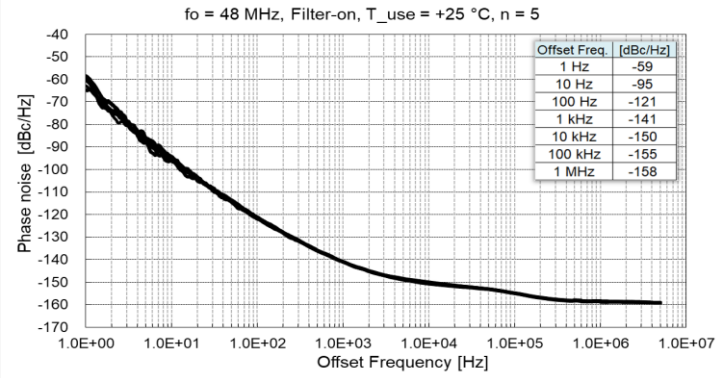


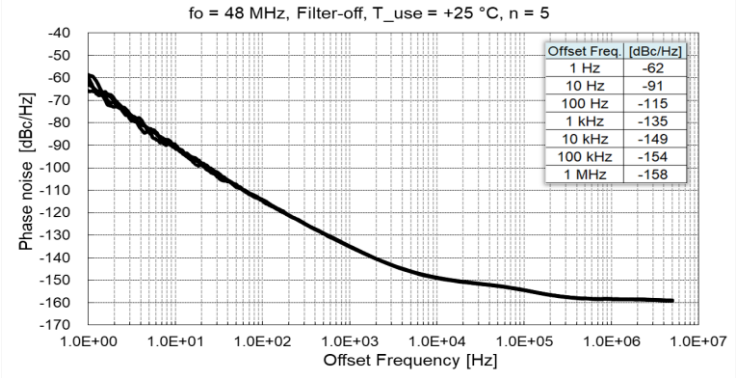
6) fo = 38.4 MHz





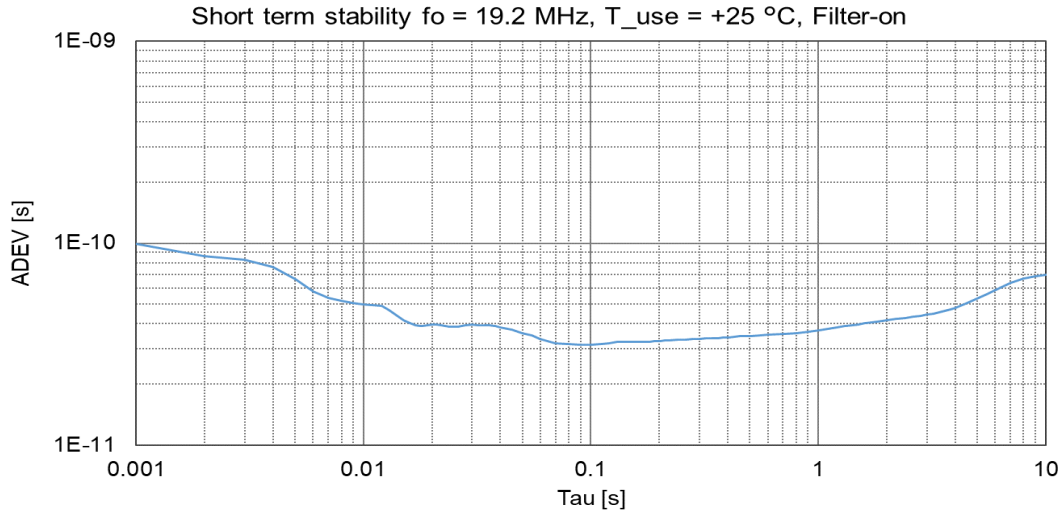
7) fo = 48 MHz

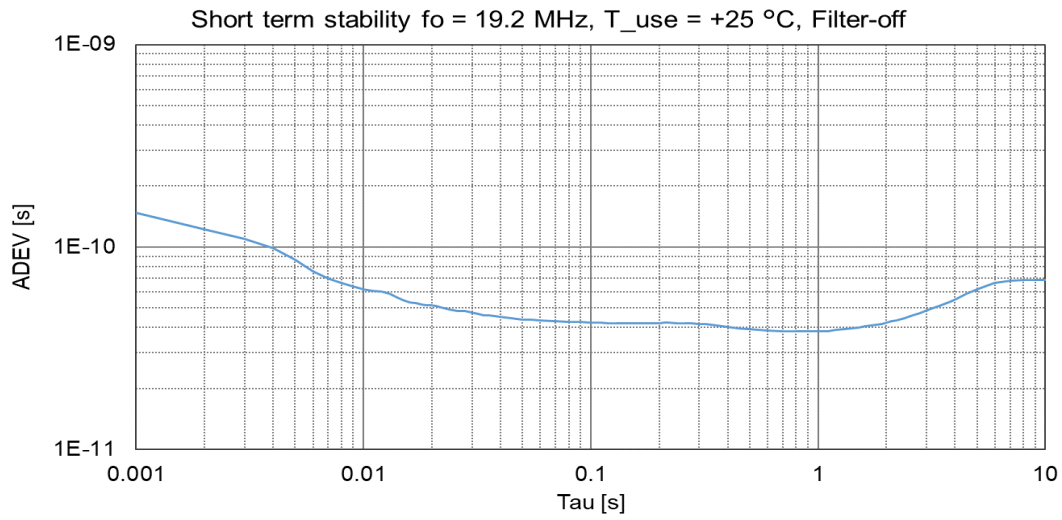




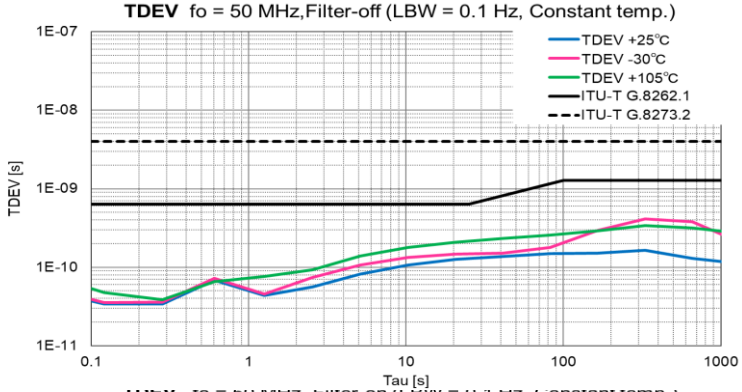
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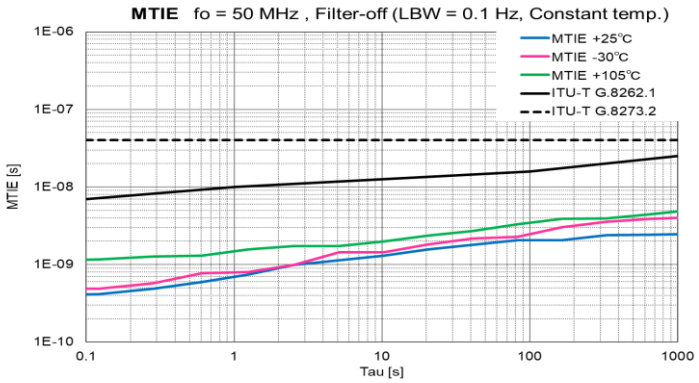
(7-11) Short Term Stability

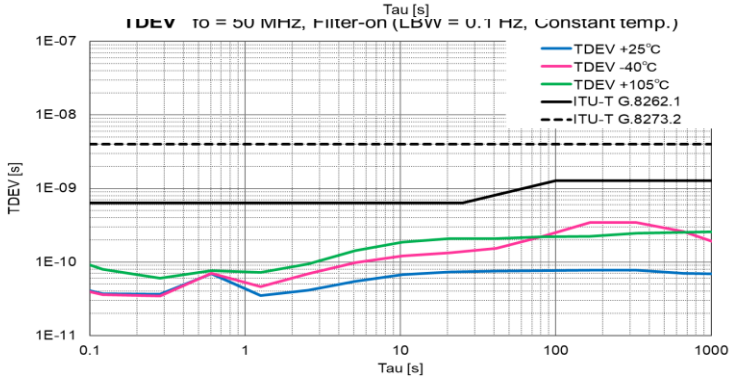


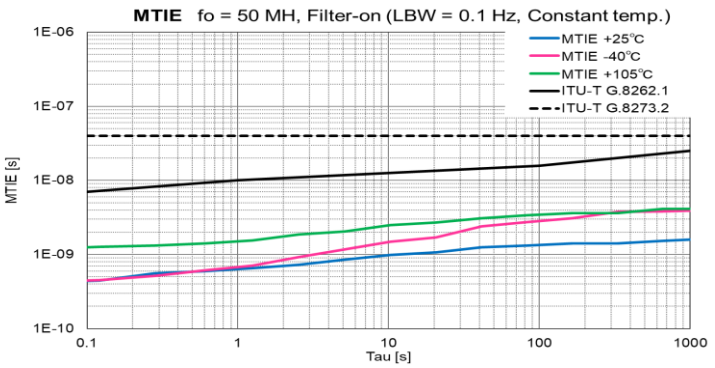


(7-12) Wander



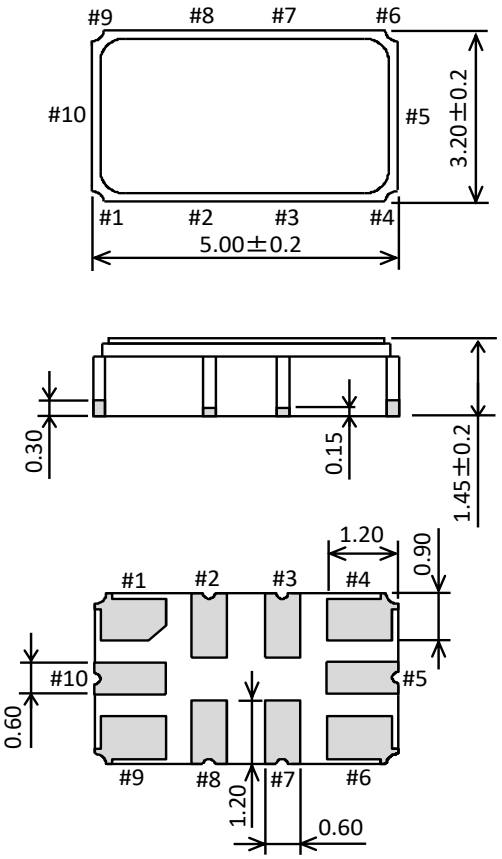




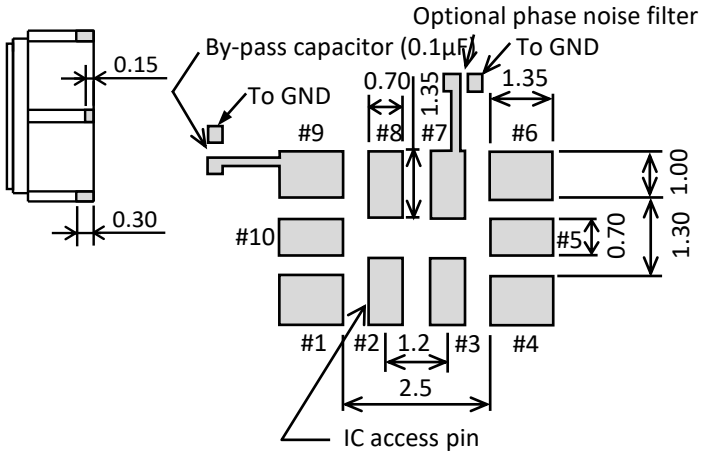


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[8] Outline Drawing unit: mm



[9] Recommended Foot Print unit: mm



(TOP View)

Pin #	Connection
1	N.C.
2	N.C.
3	OE
4	GND
5	N.C.
6	OUT
7	N.C. or Filter
8	N.C.
9	V_{CC}
10	N.C.

Terminal coating : Au plating

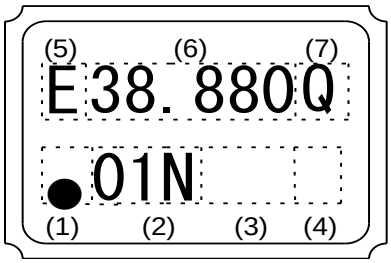
Do not connect pins marked as "N.C." with to any other pin including those marked "N.C."

For proper operation, connect a 0.1 µF by-pass capacitor between V_{CC} - GND as near as possible to the power source terminal of the oscillator.

For optional phase noise filter - ON, connect a 0.1 µF capacitor between Filter (pin 7) - GND.

If the OE Function is not used, We recommend connecting OE (pin 3) to V_{CC} (pin 9).
This TCXO can operate with pin #3, OE, as N.C.

Marking



(1) Pin 1 identifier
(2) TCXO model ID [01N]
(3) TCXO Lot No. (3 digits)
(4) Location code
(5) Epson logo Mark [E]
(6) Frequency data
(7) TG5032CKN code : Q
TG5032SKN code : R



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[10] Moisture Sensitivity Level , Electro-Static Discharge

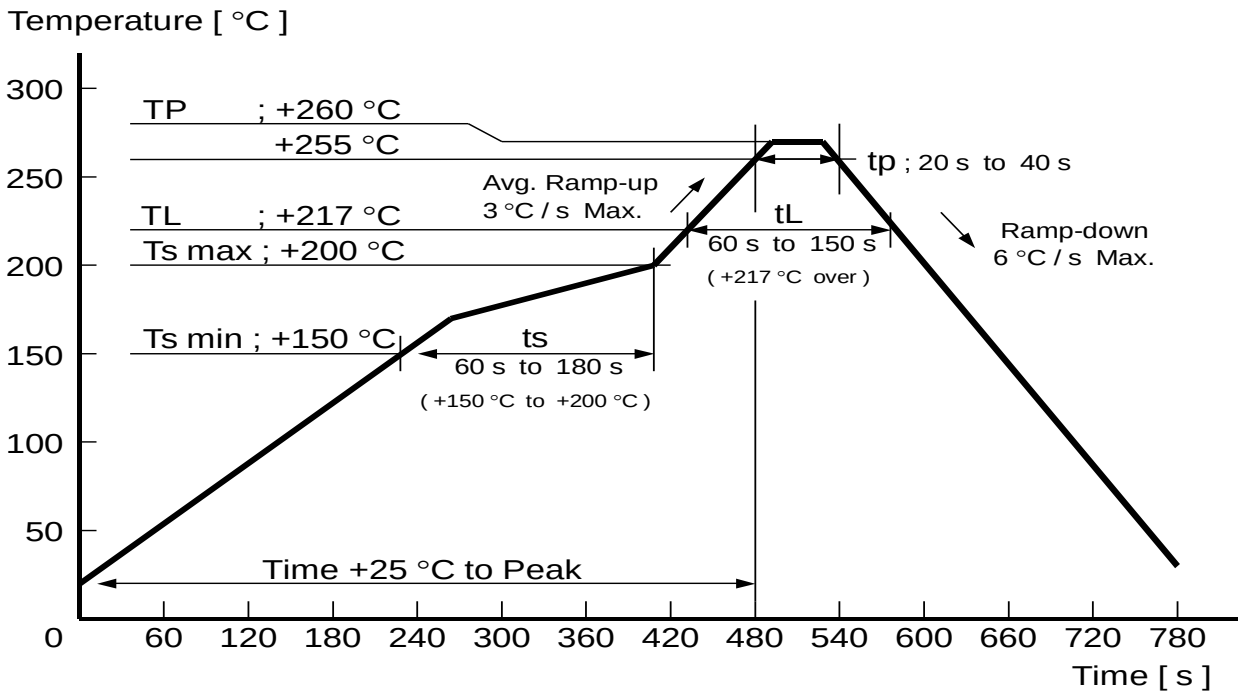
(1)Moisture Sensitivity Level (MSL)

Parameter	Specifications	Conditions
MSL	LEVEL1	JEDEC J-STD-020D

(2)Electro-Static Discharge (ESD)

Parameter	Specifications	Conditions
Human Body Model (HBM)	2 000 V Min.	IEC 60749-26 Ed.2.0:2006(b), 100 pF, 1.5 kΩ, 3 times
Machine Model (MM)	200 V Min.	IEC 60749-27 Ed.2.0:2006(b), 200 pF, 0 Ω, 1 time

[11] Reflow Profile (follow to IPC / JEDEC J-STD-020D.1)



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[12] Packing Information

(1) Quantity

For this product series, the standard for the last two digits of the product number is "00", 1 000 pcs/Reel.

TG5032CKN: X1G006021xxxx14
TG5032SKN: X1G006031xxxx14

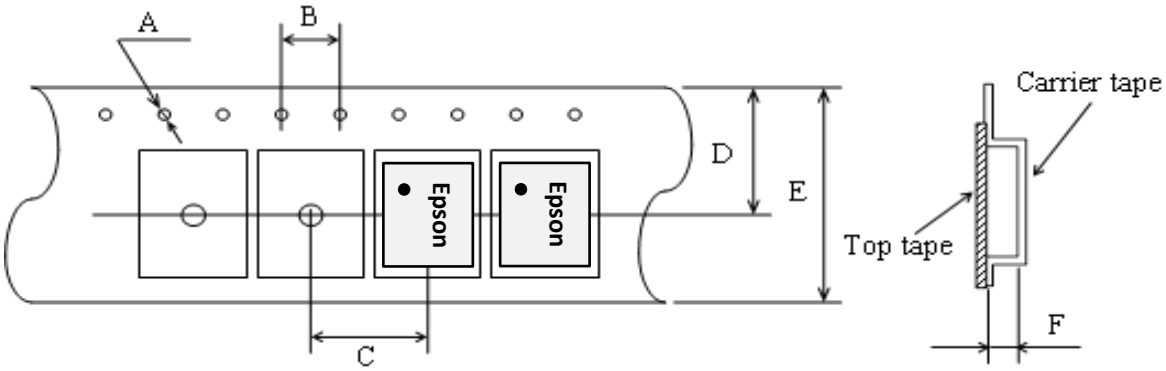
(2) Taping Specification

Subject to EIA-481, IEC-60286 and JIS C0806

1) Tape dimensions

Carrier Tape Material: PS (Polystyrene)

Top Tape Material: PET (Polyethylene Terephthalate) + PE (Polyethylene)

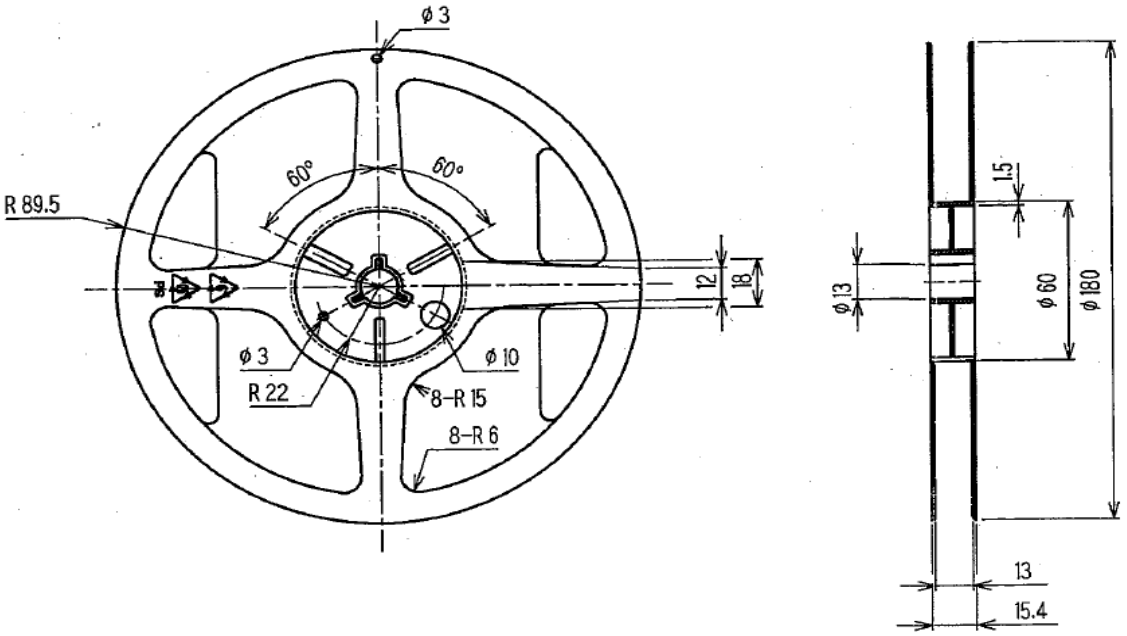


Symbol	A	B	C	D	E	F
Value	ϕ1.5	4.0	8.0	7.25	12.0	1.7

2) Reel dimensions

Center Material: PS (Polystyrene)

Reel Material: PS (Polystyrene)



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[13] Handling Precautions

Prior to using this product, please carefully read the section entitled “Precautions” on our Web site (https://www5.epsondevice.com/en/information/#precaution) for instructions on how to handle and use the product properly to ensure optimal performance of the product in your equipment.
Before using the product under any conditions other than those specified therein, please consult with us to verify and confirm that the performance of the product will not be negatively affected by use under such conditions.

In addition to the foregoing precautions, in order to avoid the deteriorating performance of the product, we strongly recommend that you DO NOT use the product under ANY of the following conditions:

(1) Do not expose this product to excessive mechanical shock or vibration.
(2) This product can be damaged by mechanical shock during the soldering process depending on the equipment used, process conditions, and any impact forces experienced. Always follow appropriate procedures, particularly when changing the assembly process in any way and be sure to follow applicable process qualification standards before starting production.
(3) These devices are sensitive to ESD, use appropriate precautions during handling, assembly, test, shipment, and installation.
(4) This product contains semiconductor content that should not be exposed to electromagnetic waves.
(5) The use of ultrasonic technology for cleaning, bonding, etc. can damage the Xtal unit inside this product. Please carefully check for this consideration before using ultrasonic equipment for volume production with this product.
(6) Noise and ripple on the power supply may have undesirable affects on operation and cause degradation of phase noise characteristics. Evaluate the operation of this device with appropriate power supplies carefully before use.
(7) When applying power, ensure that the supply voltage increases monotonically for proper operation. On power done, do not reapply power until the supplies, bypass capacitors, and any bulk capacitors are completely discharged since that may cause the unit to malfunction.
(8) Aging specifications are estimated from environmental reliability tests and expected frequency variation over time. They do not provide a guarantee of aging over the product lifecycle.
(9) The metal cap on top of the device is directly connected to the GND terminal (pin #2). Take necessary precautions to prevent any conductor not at ground potential from contacting the cap as that could cause a short circuit to GND.
(10) Do not route any signal lines, supply voltage lines, or GND lines underneath the area where the oscillators are mounted including any internal layers and on the opposite side of the PCB. To avoid any issues due to interference of other signal lines, please take care not to place signal lines near the product as this may have an adverse affect on the performance of the product.
(11) A bypass capacitor of the recommended value(s) must be connected between the V_{CC} and GND terminals of the product. Whenever possible, mount the capacitor(s) on the same side of the PCB and as close to the product as possible to keep the routing traces short.
(12) Power supply connections to V_{CC} and GND pins should be routed as thick as possible while keeping the high frequency impedance low in order to get the best performance.
(13) The use of a filter or similar element in series with the power supply connections to protect from electromagnetic radiation noise may increase the high frequency impedance of the power supply line and may cause the oscillator to not operate properly. Please verify the design to ensure sufficient operational margin prior to use.
(14) Keep PCB routing from the output terminal(s) to the load as short as possible for best performance.
(15) Do not short the output to GND as that will damage the product. Always use with an appropriate load resistor connected.
(16) Product failures during the warranty period only apply when the product is used according to the recommended operating conditions described in the specifications. Products that have been opened for analysis or damaged will not be covered. It is recommended to store and use in normal temperature and humidity environments described in the specifications to ensure frequency accuracy and prevent moisture condensation. If the product is stored for more than one year, please confirm the pin solderability prior to use.
(17) If the oscillation circuit is exposed to condensation, the frequency may change or oscillation may stop. Do not use in any conditions where condensation occurs.
(18) Do not store or use the product in an environment where it can be exposed to chemical substances that are corrosive to metal or plastics such as salt water, organic solvents, chemical gasses, etc. Do not use the product when it is exposed to sunlight, dust, corrosive gasses, or other materials for long periods of time.
(19) When using water-soluble solder flux make sure to completely remove the flux residue after soldering. Pay particular attention when the residues contain active halogens which will negatively affect the product and its performance.

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At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

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In order provide high quality and reliable products and services than meet customer needs, Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired IATF 16949 certification that is requested strongly by major manufacturers as standard.

IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

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RoHS Compliant	**●Complies with EU RoHS directive.** *About the products without the Pb-free mark. Contains Pb in products exempted by EU RoHS directive (Contains Pb in sealing glass, high melting temperature type solder or other)

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   Customer shall implement necessary and proper safety design and measures (including redundant design, malfunction prevention design, etc.) to ensure reliability and safety before using the products in/with customer’s Equipment.

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