Hosonic

VCXO

D3SV Series 3.2×2.5×1.2mm



Features

- SMD package
- Tight tolerance performance with voltage IC control
- Wide pull range and good linearity

Applications

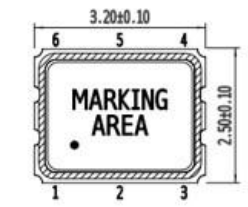
- ADSL
- Set-top box
- Base stations

Electrical Specifications

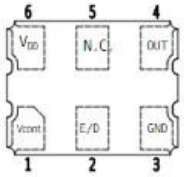
Item	Symbol	Condition	D3SV
Frequency Range	f_{nom}	-	1.75 ~ 61.44MHz
Output Type	-	-	CMOS
Output Load	-	-	15pF. or specify
Frequency Calibration	-	At 25℃	± 15ppm
Frequency Stability	f_{stb}	Over T_{opr}	± 25ppm. or specify
Stability vs. power change	-	V_{DD}±5%	± 5ppm
Stability vs. load change	-	15pF±10%	± 3ppm
Pullability	-	Over Vcont	± 100ppm. or specify
Control Voltage Range	Vcont	-	0 ~ V_{DD} ($1/2V_{DD}$ nominal)
Linearity	-		10% max.
Operating Temperature Range	T_{opr}	-	-20 ~ +70℃ . or specify
Storage Temperature Range	T_{stg}	-	-40 ~ +85℃
Power Supply Voltage	V_{DD}	-	3.3V±5%. 2.5V±5% Option
Supply Current	I_{DD}	At 15pF Load	30mA max.
Rise Time / Fall Time	T_r/T_f	20%V_{DD} ~ 80%V_{DD}	5ns max.
Output Voltage	V_{OH}	-	90%V_{DD} min.
	V_{OL}	-	10%V_{DD} max.
Output Symmetry	SYM	At 1/2 V_{DD}	45% ~ 55%
Start-up time	-	-	10ms max.
Aging (At 25℃)	-	-	± 3 ppm / year max.
Pin 2. Enable/Disable function	E/D	-	Pin 2="High" or "open".....output active at pin 4 Pin 2="Low".....high impedance at pin 4

Mechanical Dimensions

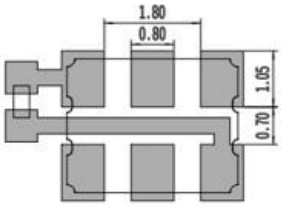
Unit: (mm)



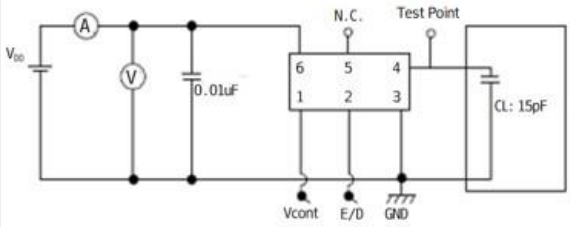
Pin Connection (Top View)



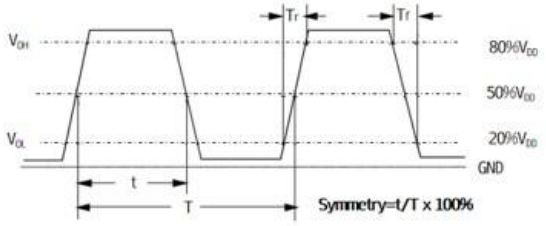
Recommended PAD Layout



Test circuit & Output Waveform



Output Waveform



** 0.01uF bypass capacitor should be placed between V_{DD} (pin 6) and GND (pin 3) to minimize power supply line noise.

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